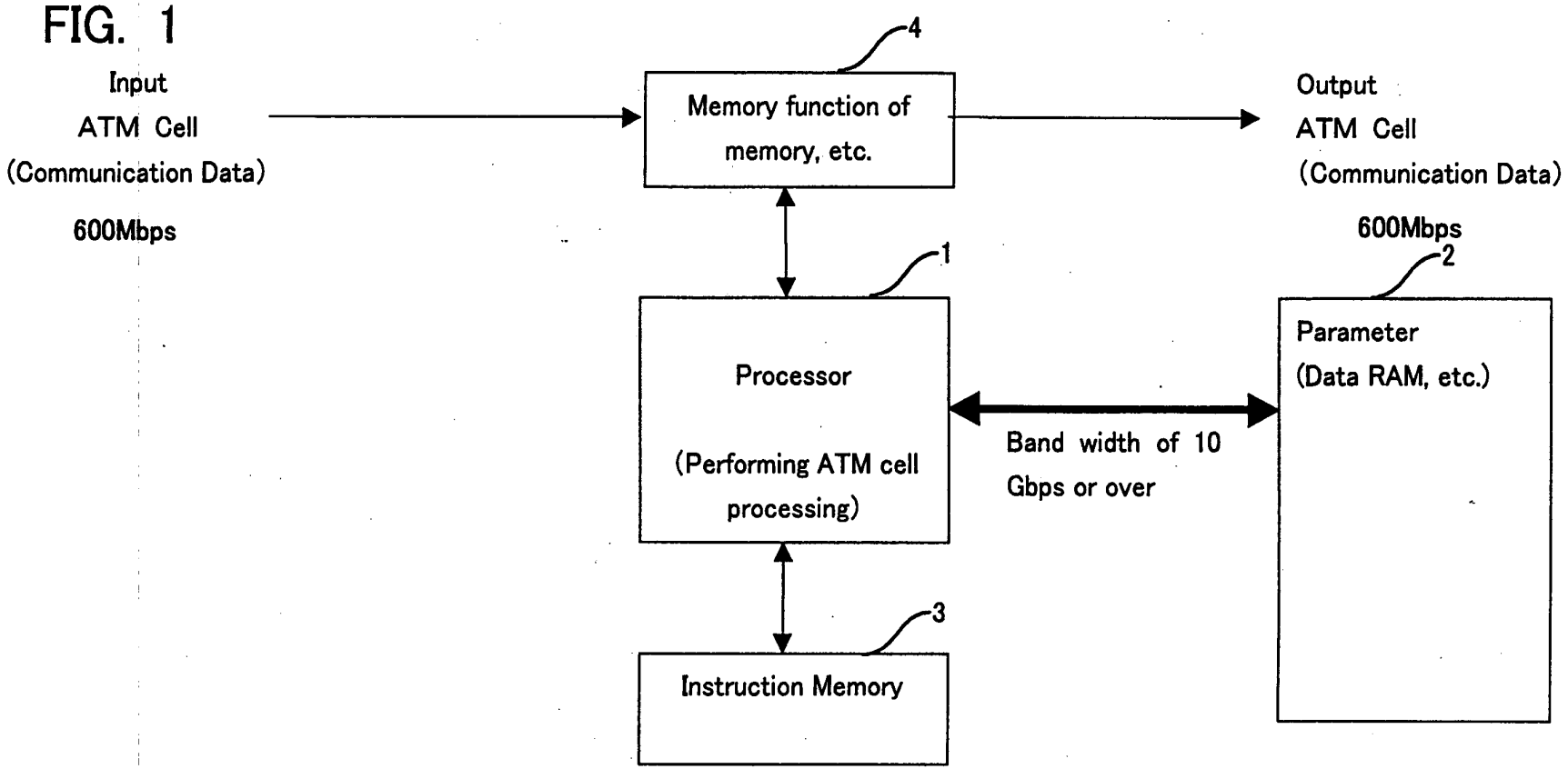
FIG. 1


Input
ATM Cell
(Communication Data)
600Mbps
4
Memory function of memory, etc.
Output
ATM Cell
(Communication Data)
600Mbps
1
Processor
(Performing ATM cell processing)
Band width of 10 Gbps or over
2
Parameter
(Data RAM, etc.)
3
Instruction Memory

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O.G. FIG.
CLASS
SUBCLASS

FIG. 2

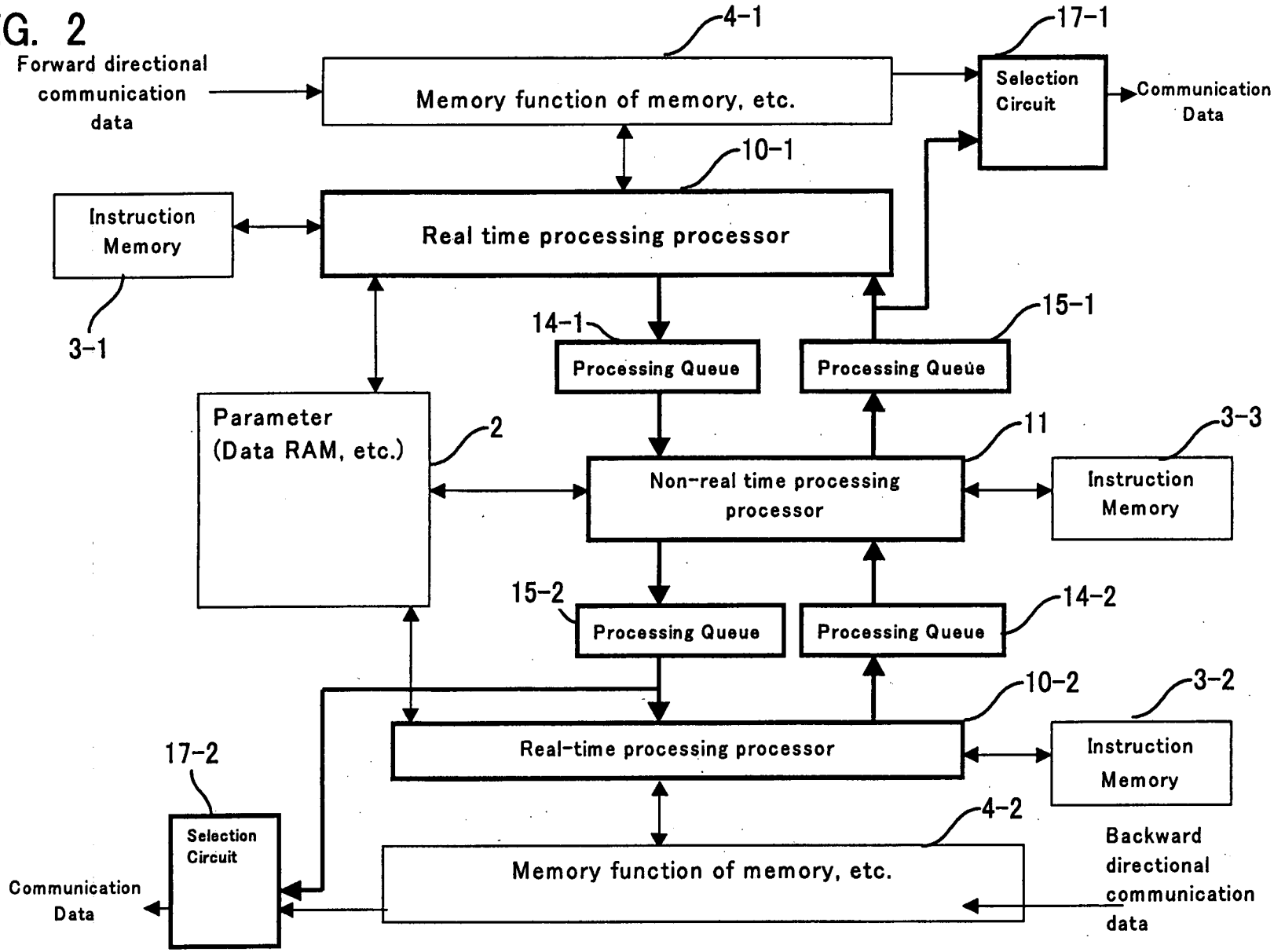

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FIG. 3

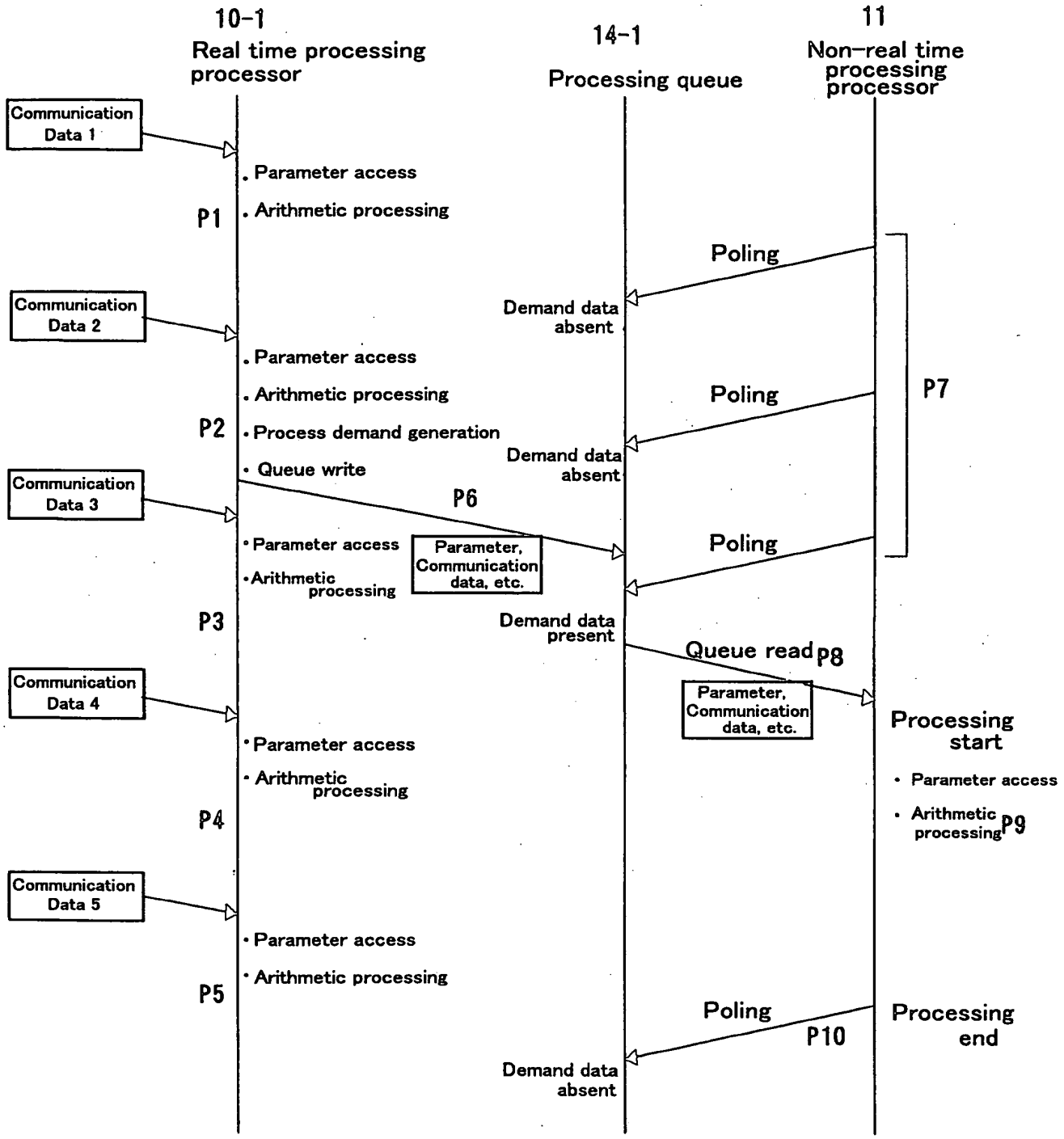

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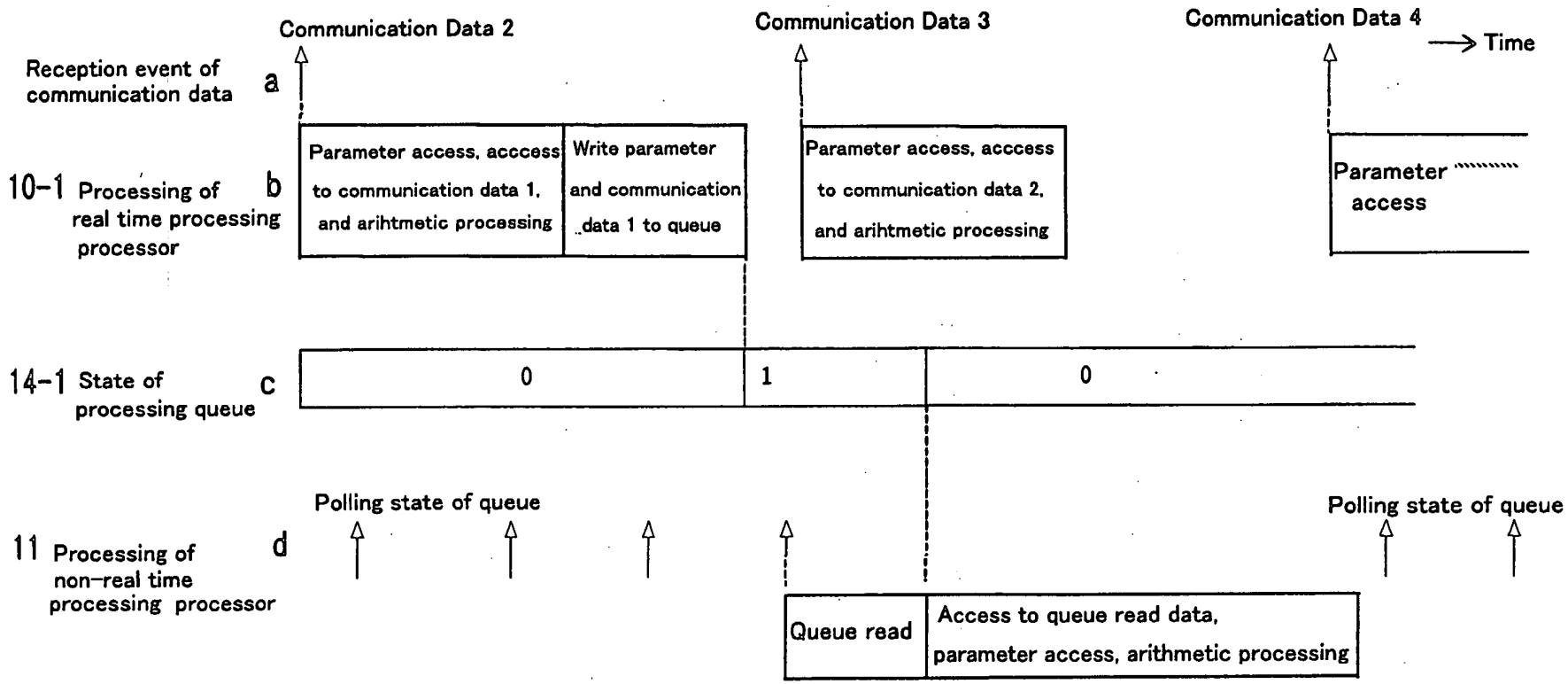
FIG. 4



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CLASS
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O.G. FIG.
CLASS
SUBCLASS

FIG. 5

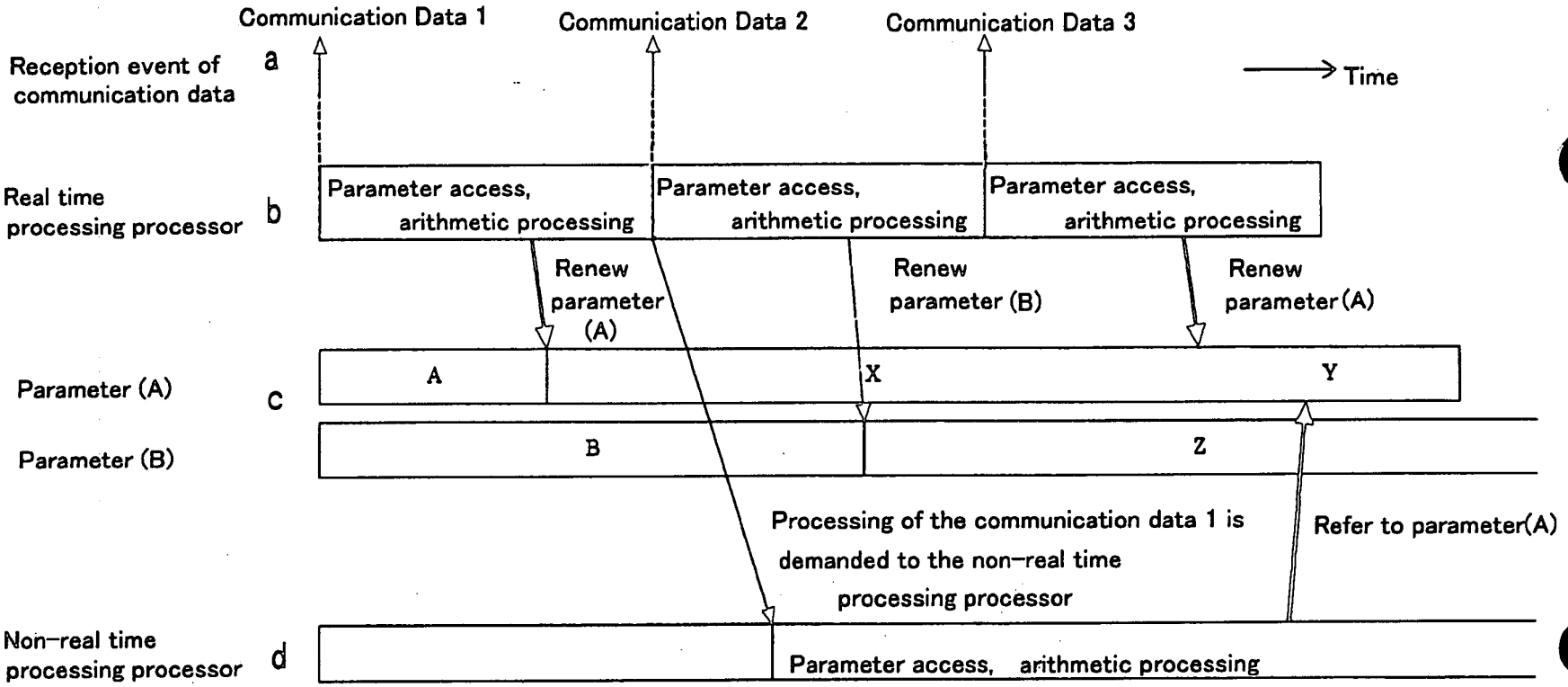

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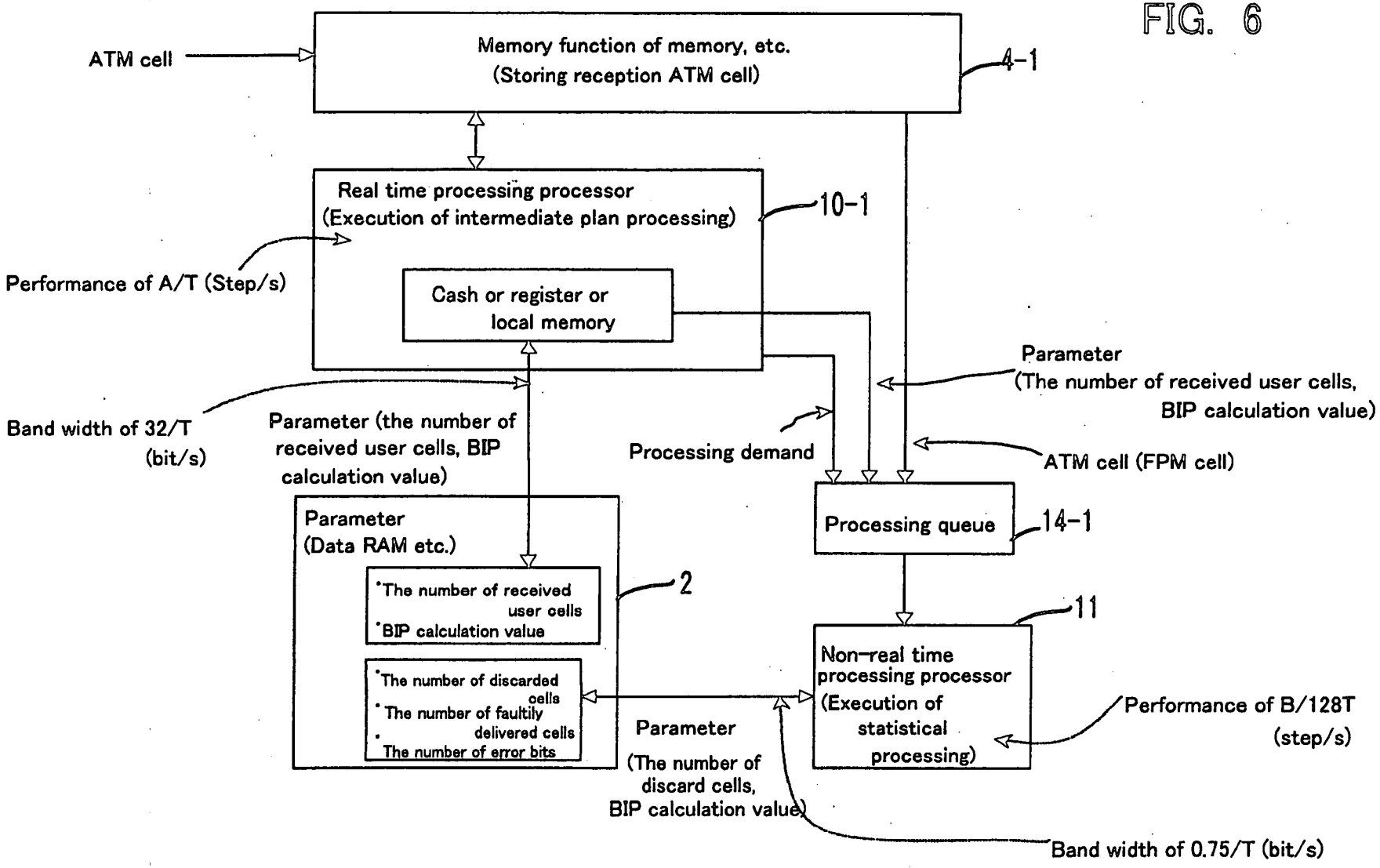
FIG. 6



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SUBCLASS

FIG. 7

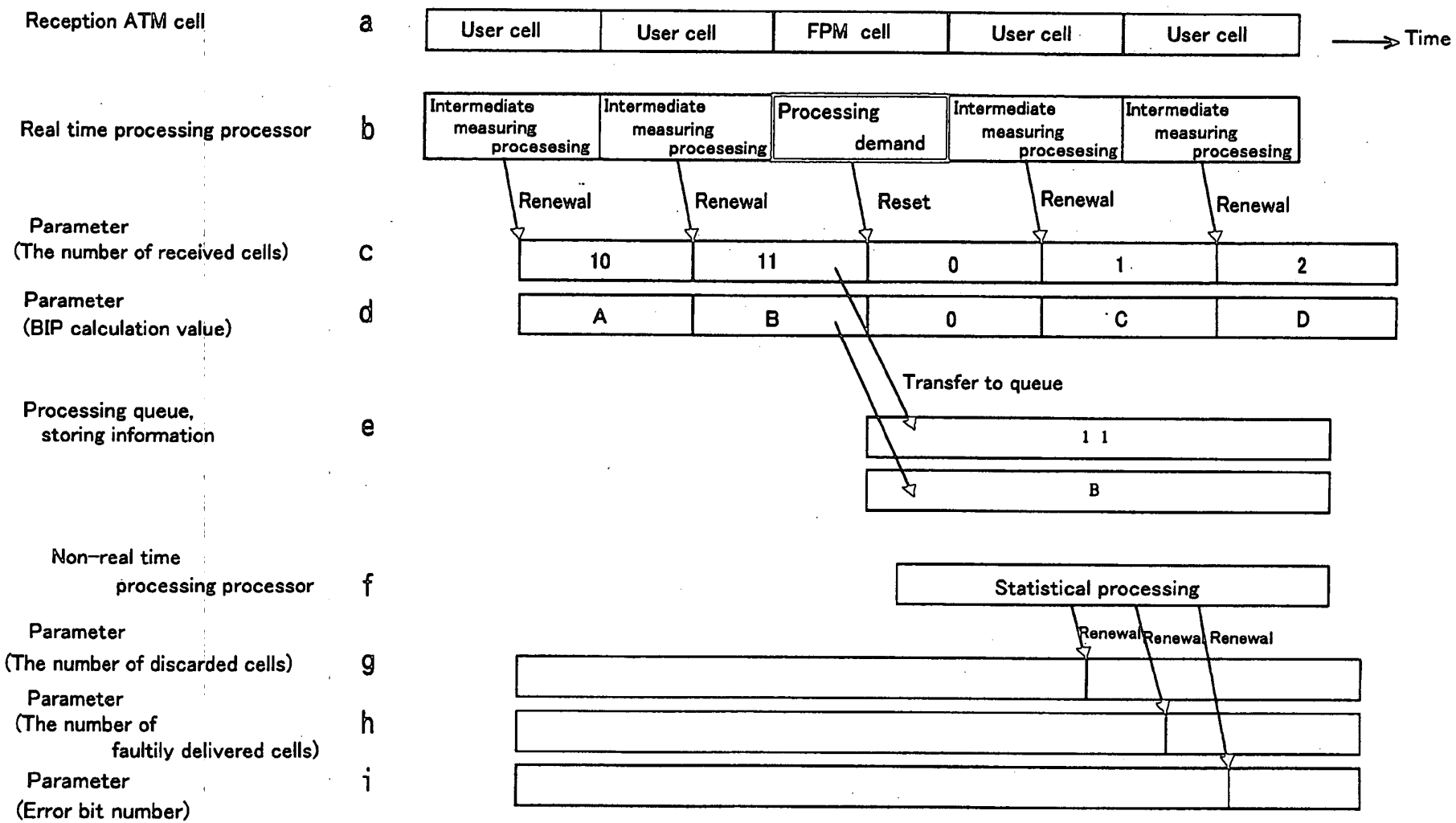

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FIG. 8A

ATM cell transmission side

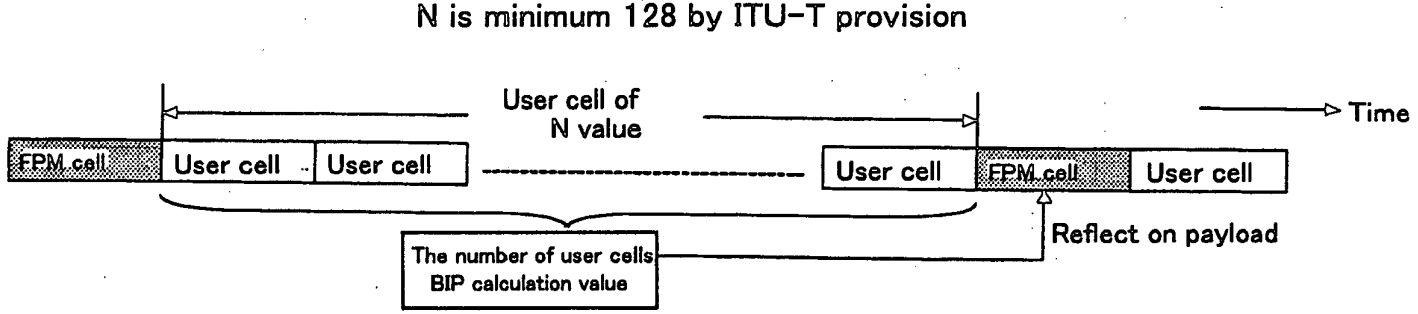


FIG. 8B

ATM cell reception side

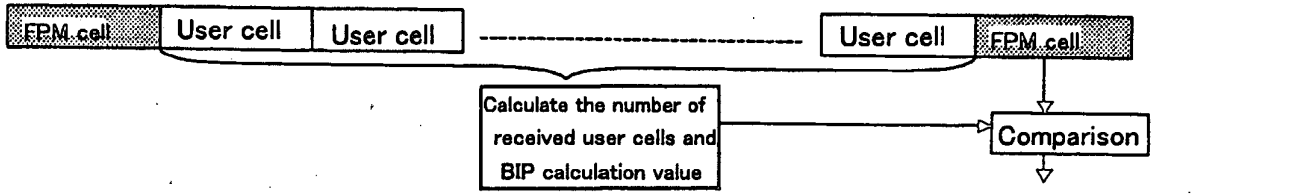
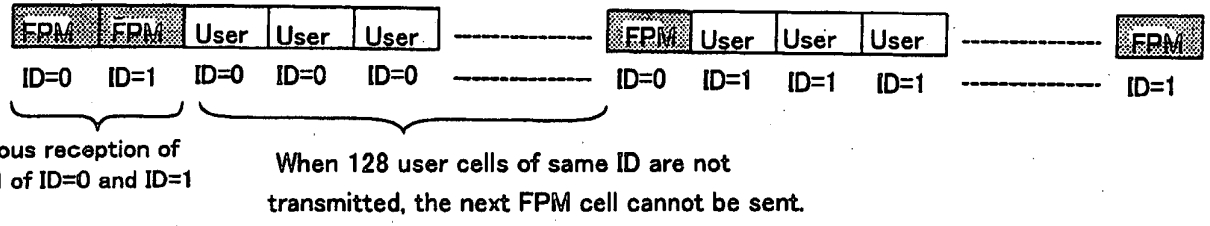


FIG. 8C

ATM cell flow



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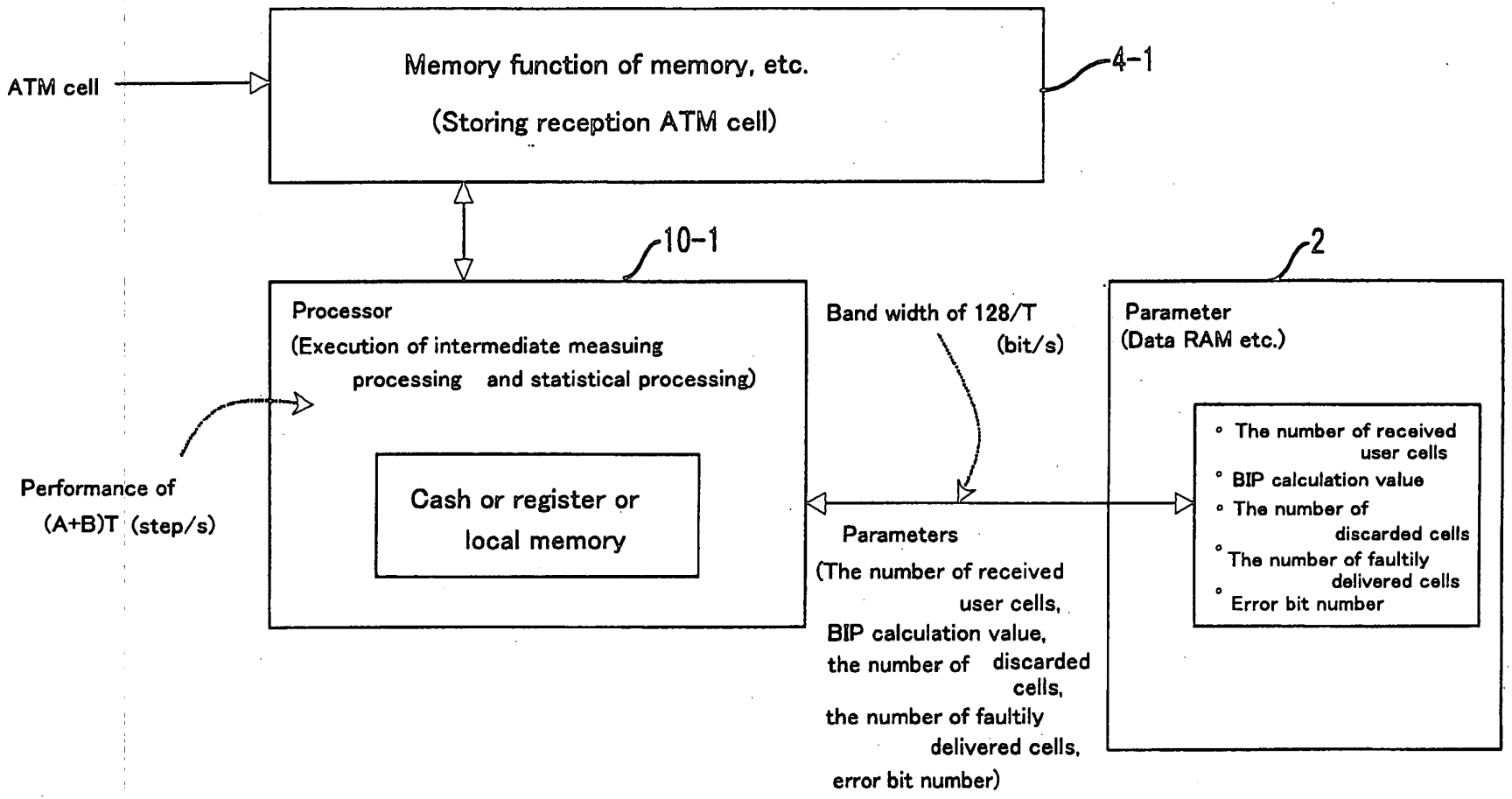
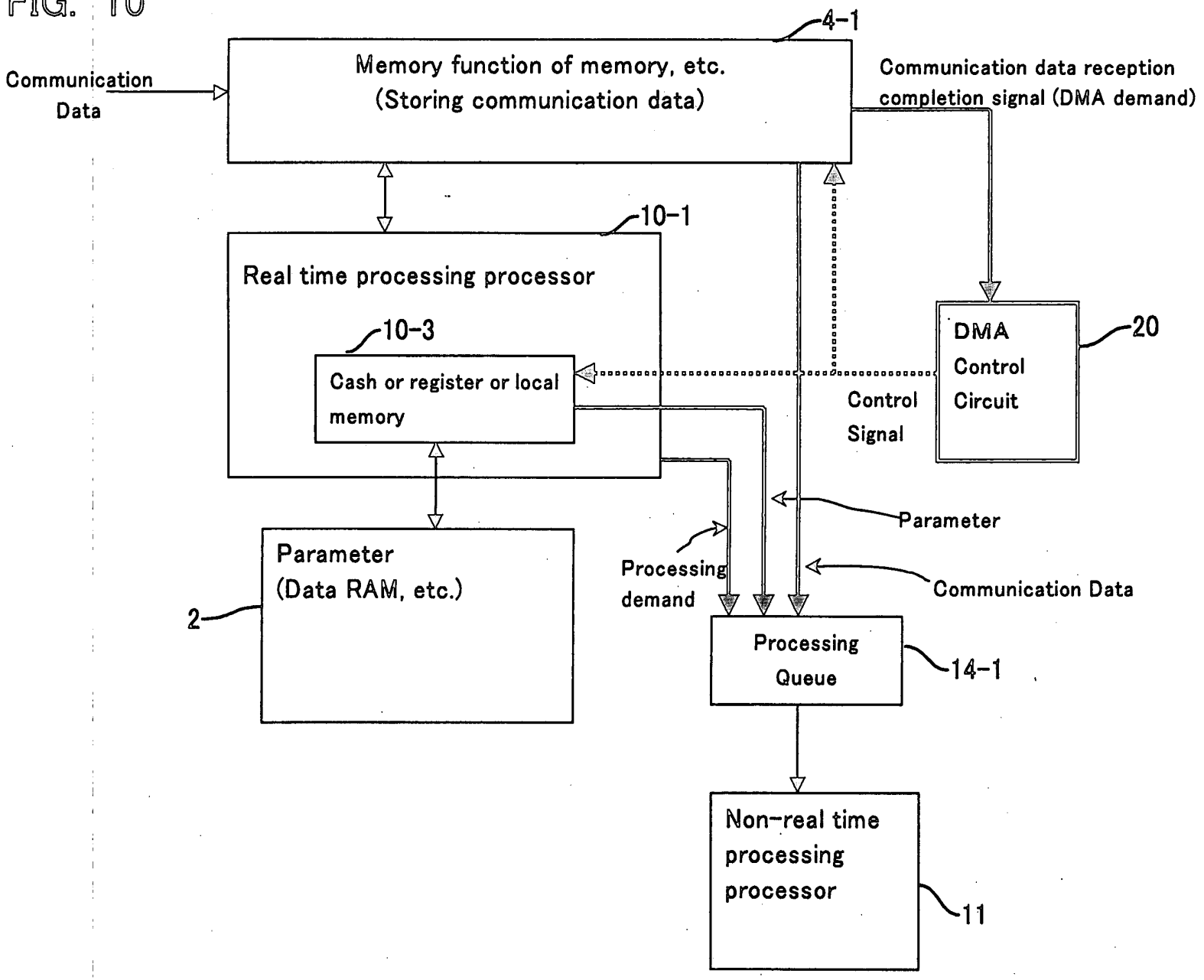
CLASS
SUBCLASS

FIG. 9



PATENT DRAWING

FIG. 10



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CLASS
SUBCLASS

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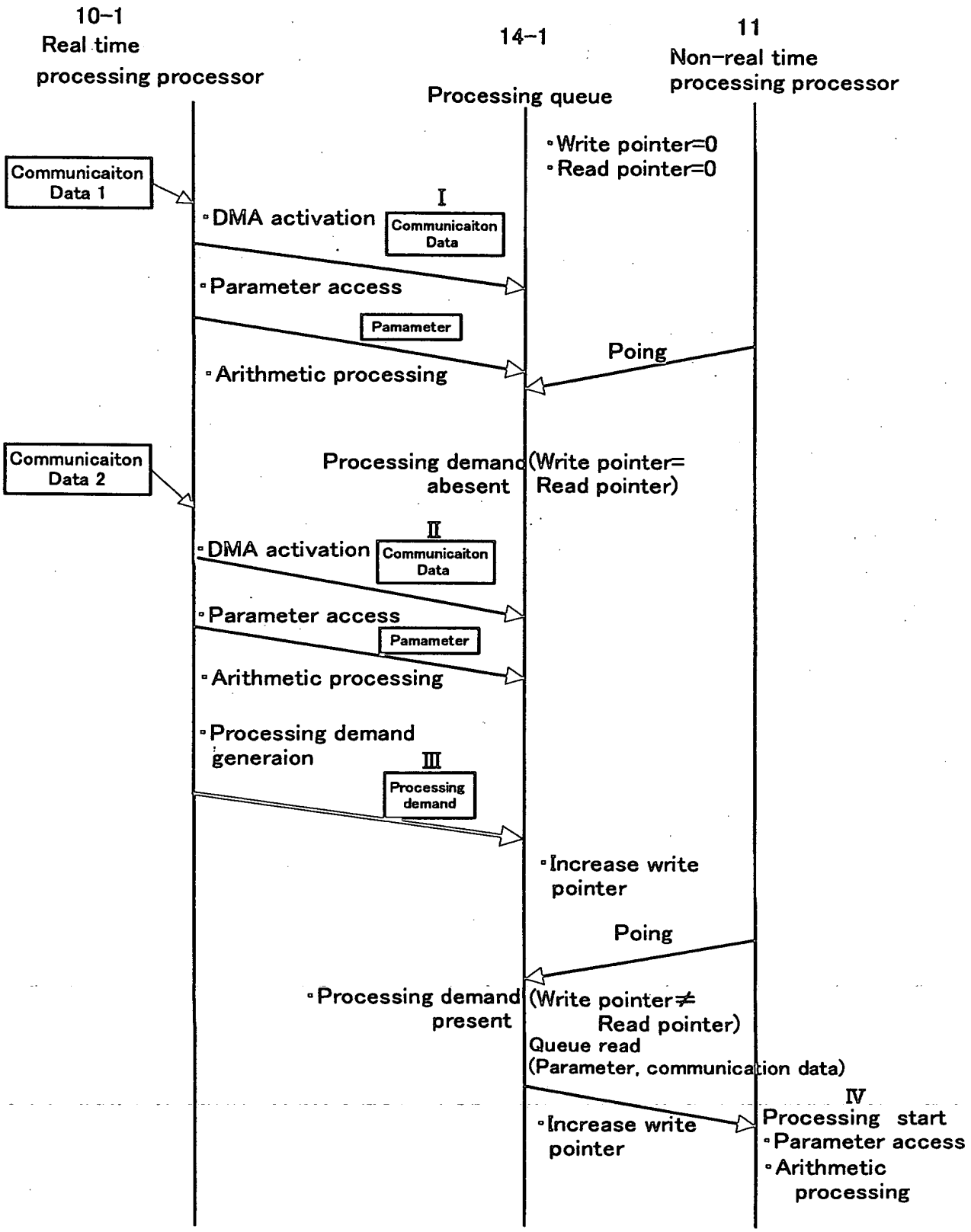
FIG. 11



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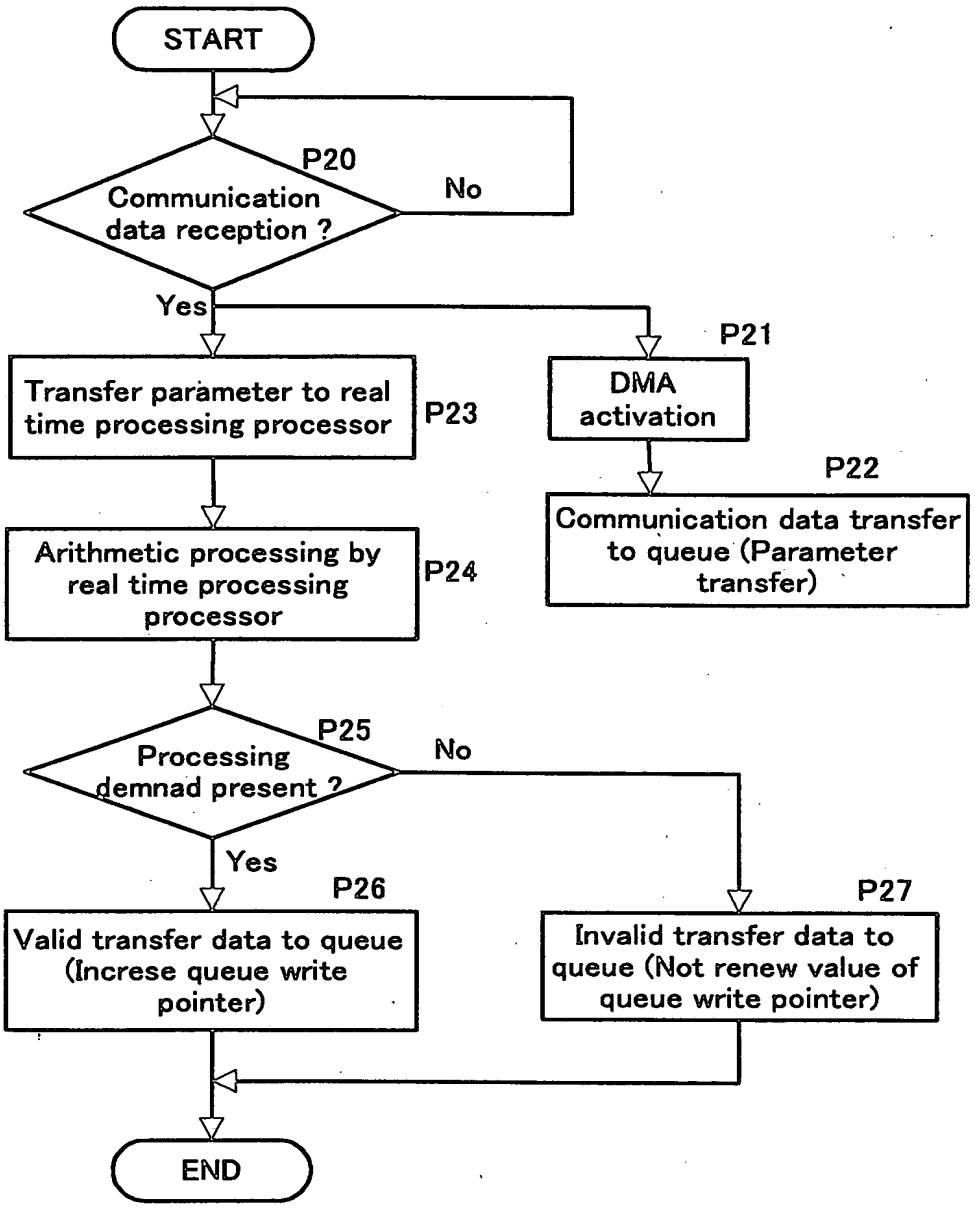
O.G. FIC
CLASS SUBCLASS

FIG. 12



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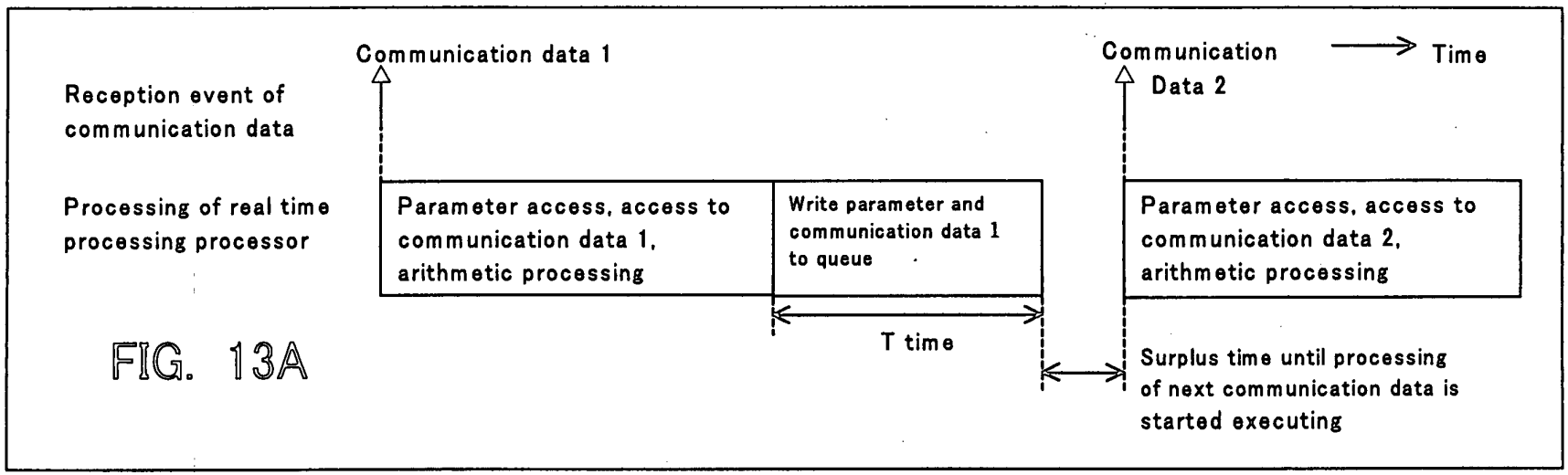


FIG. 13A

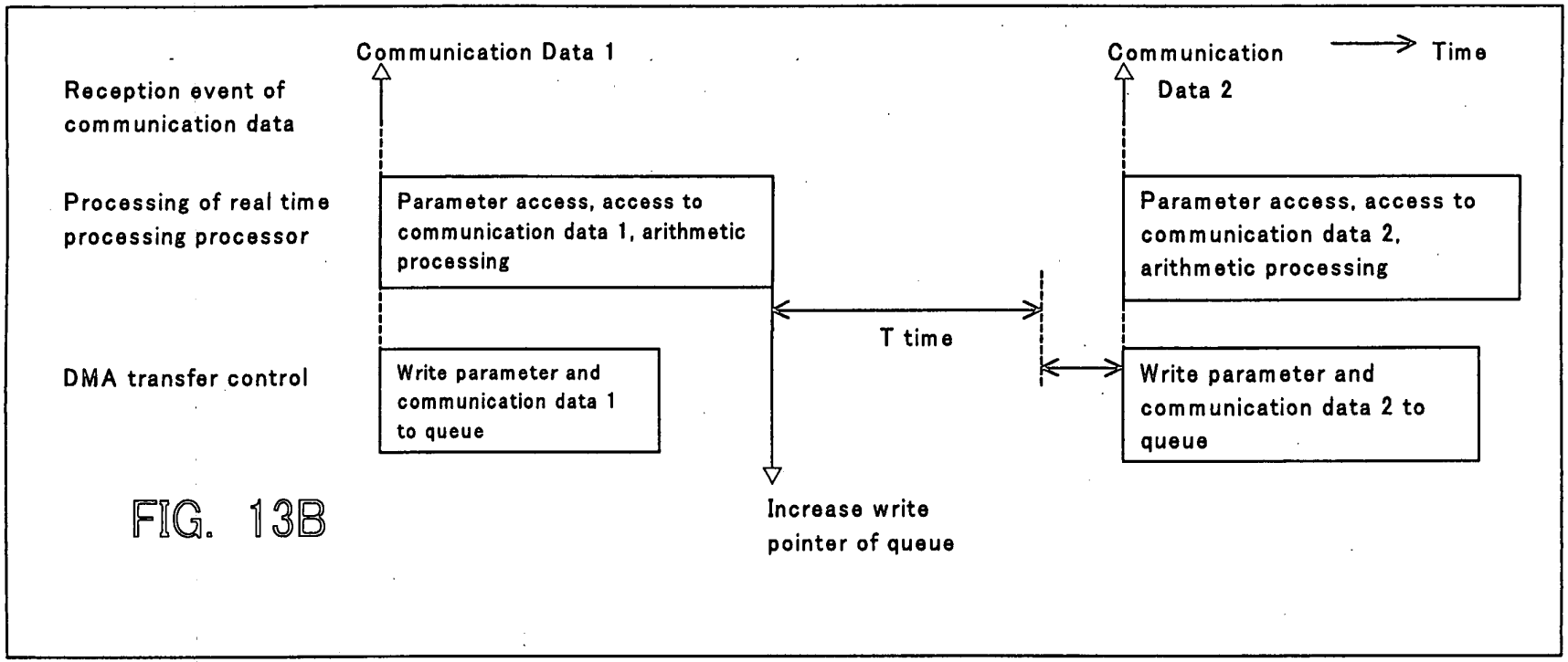
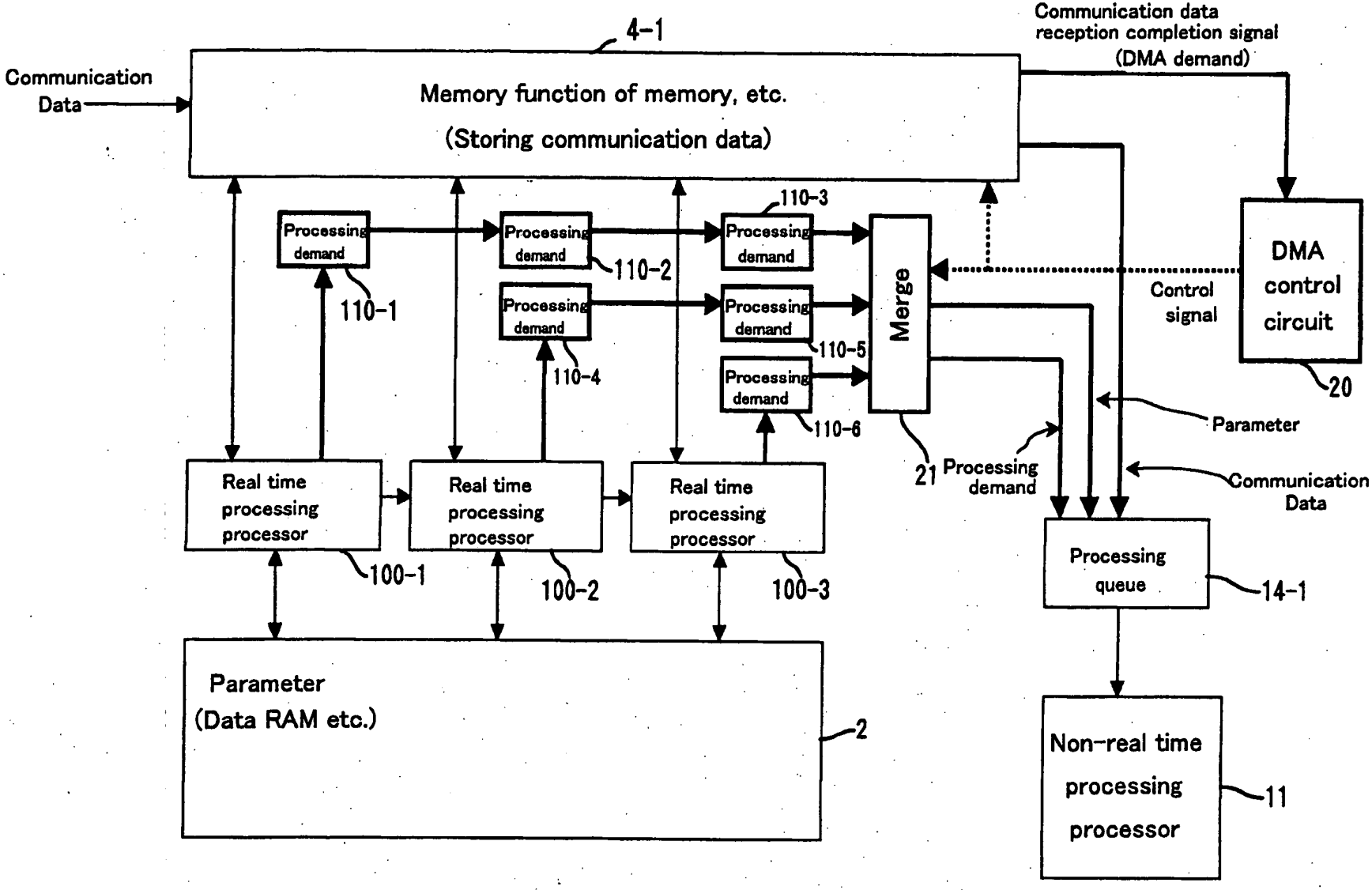


FIG. 13B

FIG. 14


4-1
Communication Data
Memory function of memory, etc.
(Storing communication data)
Communication data reception completion signal (DMA demand)
Processing demand
110-1
110-2
110-3
110-4
110-5
110-6
Merge
Control signal
DMA control circuit
20
Parameter
21
Processing demand
Communication Data
Real time processing processor
100-1
100-2
100-3
Processing queue
14-1
Parameter (Data RAM etc.)
2
Non-real time processing processor
11

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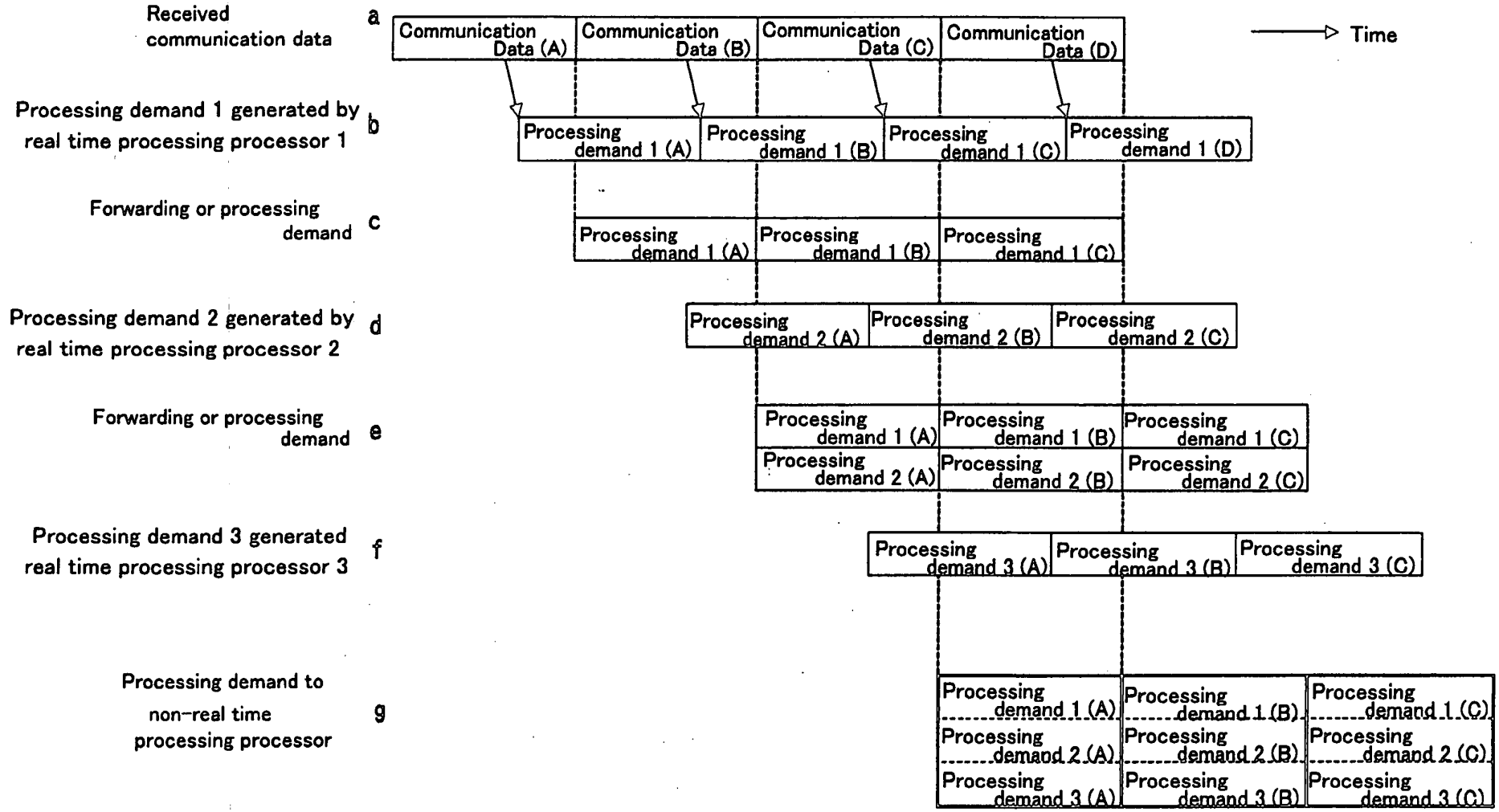
CLASS
SUBCLASS

FIG. 15



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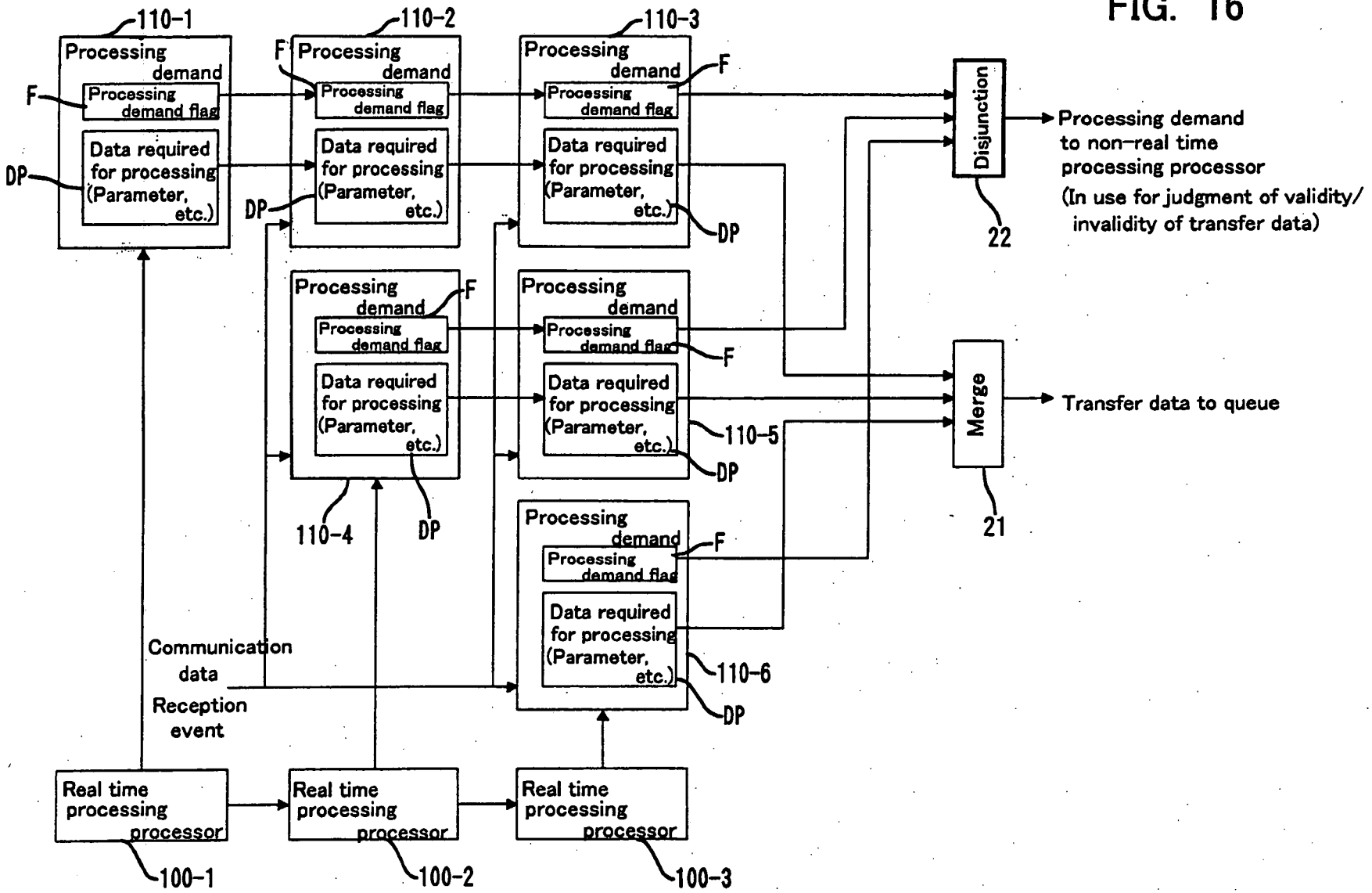
O.G. FIG. CLASS SUBCLASS

FIG. 16



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BY | CLASS | SUBCLASS
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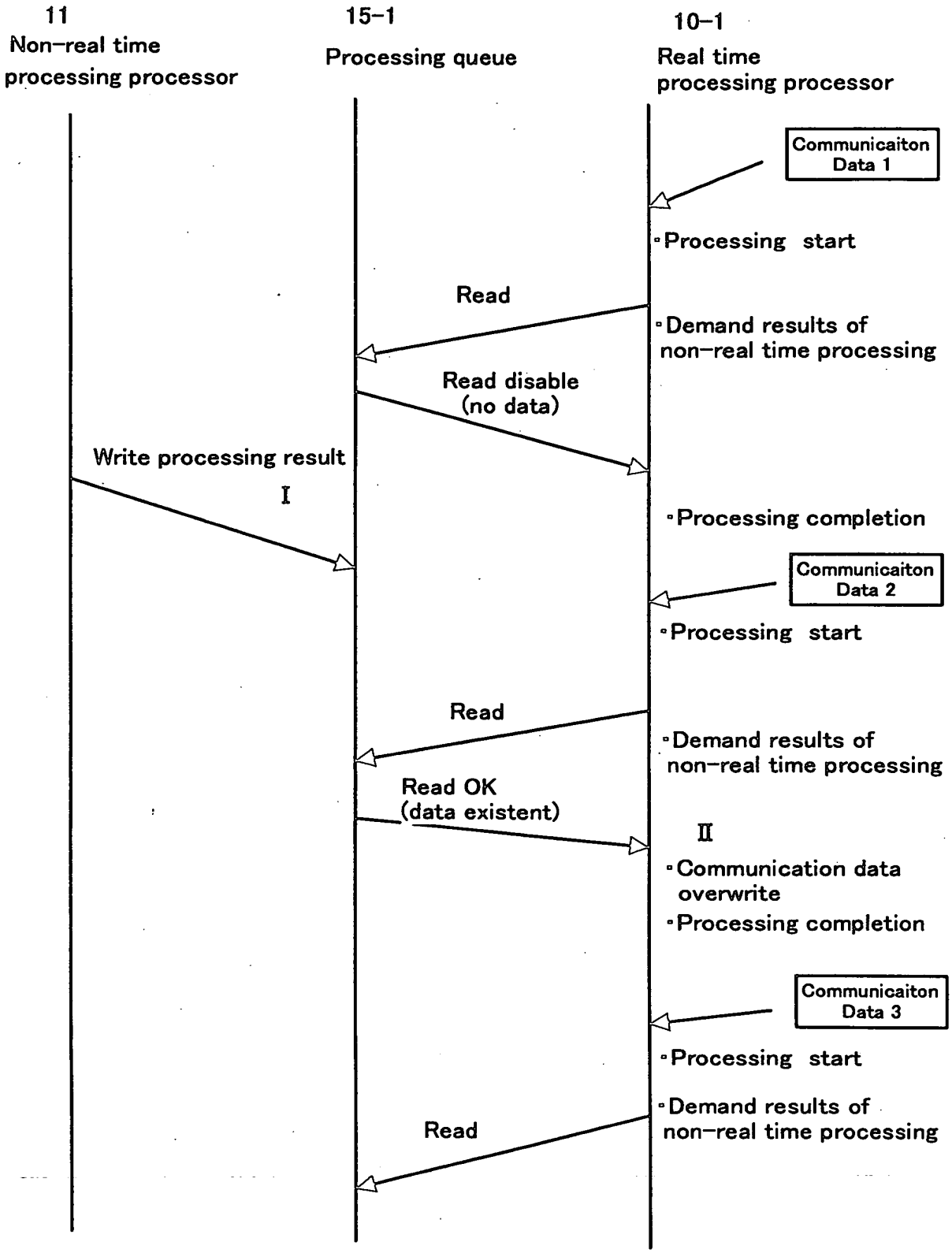
FIG. 17



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CLASS
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FIG. 18

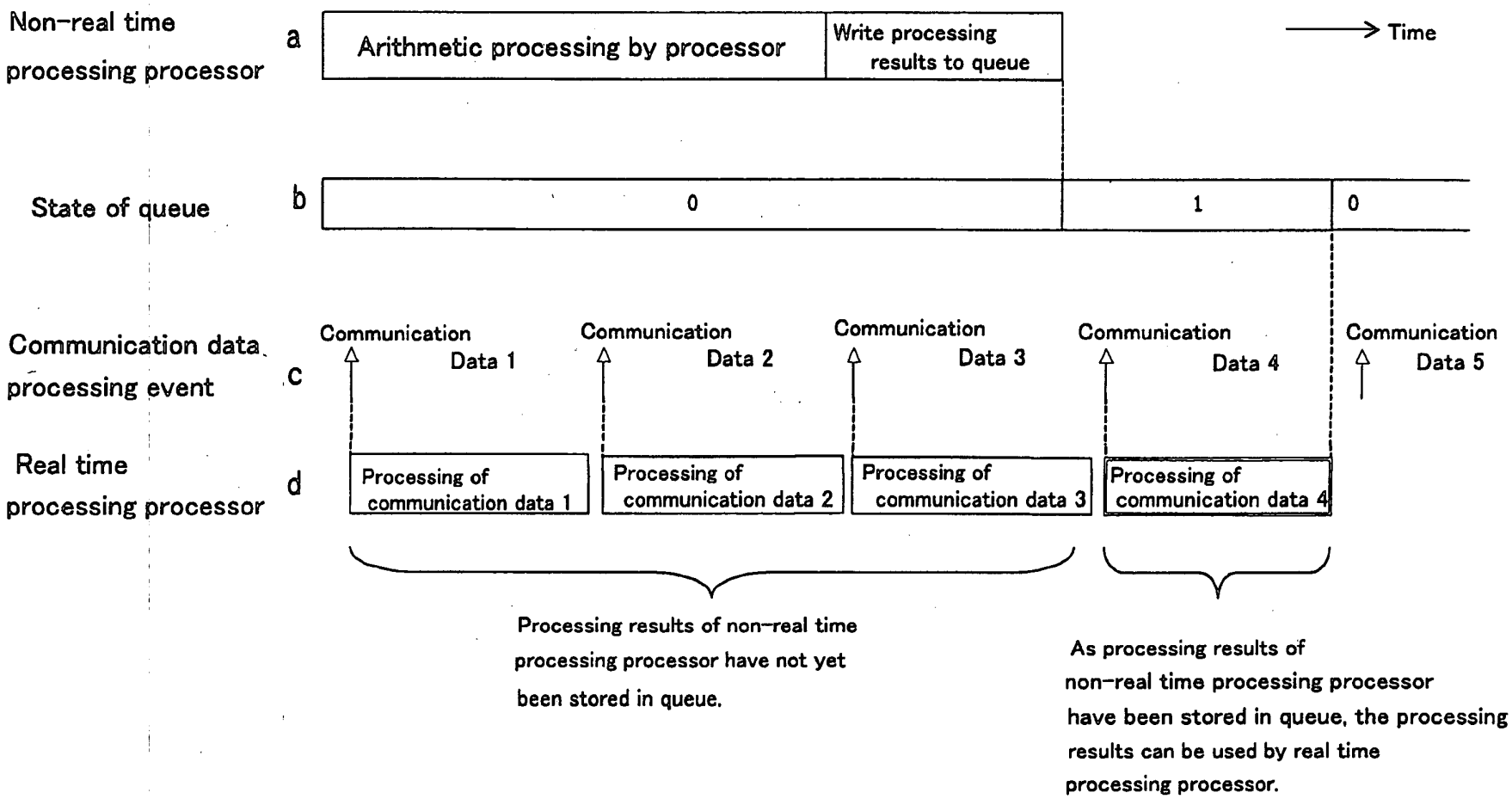

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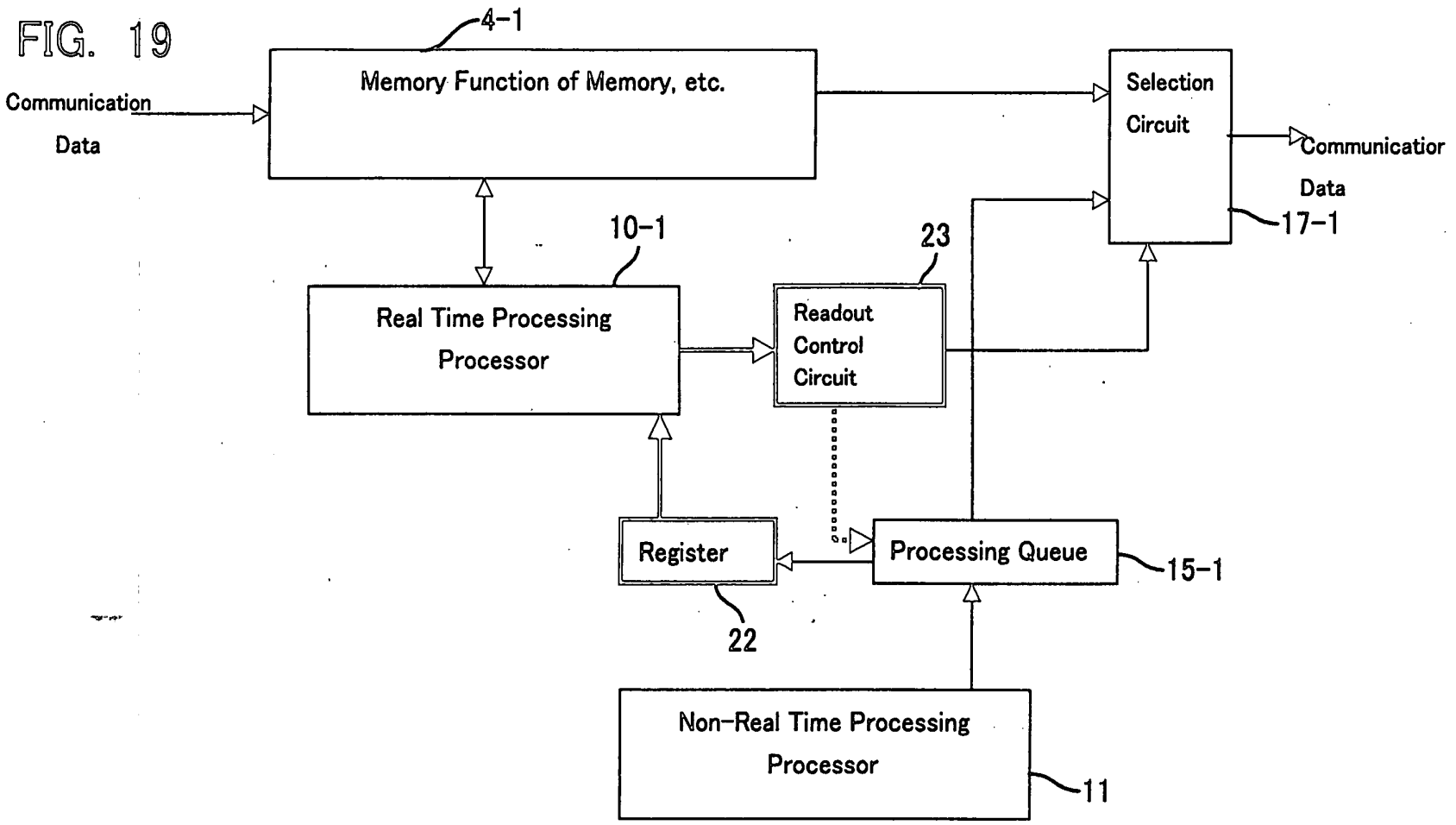
FIG. 19



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CLASS
SUBCLASS